

**A SLOTTED SEMICONDUCTOR SUBSTRATE HAVING  
MICROELECTRONICS INTEGRATED THEREON**

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**ABSTRACT**

The exemplary embodiments describe a semiconductor substrate having microelectronics integrated thereon. In one exemplary embodiment, the semiconductor substrate comprises a plurality of fluid ejecting elements positioned over a substrate. The semiconductor substrate can further comprise one or more fluid feed channel(s) formed in the substrate. The one or more fluid feed channel(s) being configured to deliver fluid to the plurality of fluid ejecting elements. The one or more fluid feed channel(s) are defined at least in part by first and second substantially parallel side walls and first and second non-parallel end walls.

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